1/5

FIG. 1A

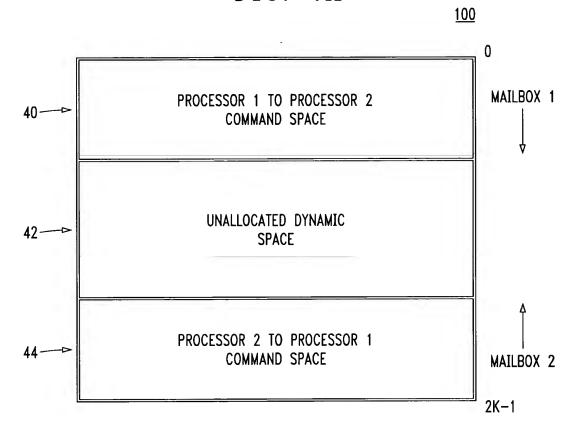
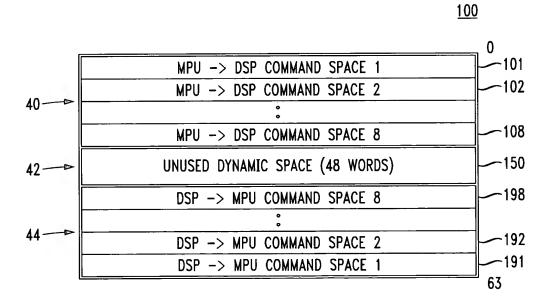


FIG. 1B



2/5

FIG. 2

200

300

MPU -> DSP COMMAND SPACE 1 **~101** 40-MPU -> DSP COMMAND SPACE 2 -102MPU -> DSP COMMAND SPACE 3 (DATA 1) -113 MPU -> DSP COMMAND SPACE 8 (DATA 6) **~118** 42-DYNAMIC SPACE 1 (DATA 7) **—157** DYNAMIC SPACE 48 (DATA 54) **—158** DSP -> MPU COMMAND SPACE 8 **—198** DSP -> MPU COMMAND SPACE 2 **—192** DSP -> MPU COMMAND SPACE 1 **~191**

FIG. 3

MPU -> DSP COMMAND SPACE 1 **~**101 -102MPU -> DSP COMMAND SPACE 2 MPU -> DSP COMMAND SPACE 8 DYNAMIC SPACE 1 (DATA 54) -168DYNAMIC SPACE 48 (DATA 7) **—167** DSP -> MPU COMMAND SPACE 8 (DATA 6) **~**188 42--183DSP -> MPU COMMAND SPACE 3 (DATA 1) -192DSP -> MPU COMMAND SPACE 2 DSP -> MPU COMMAND SPACE 1 **~191** 3/5

FIG. 4
PRIOR ART

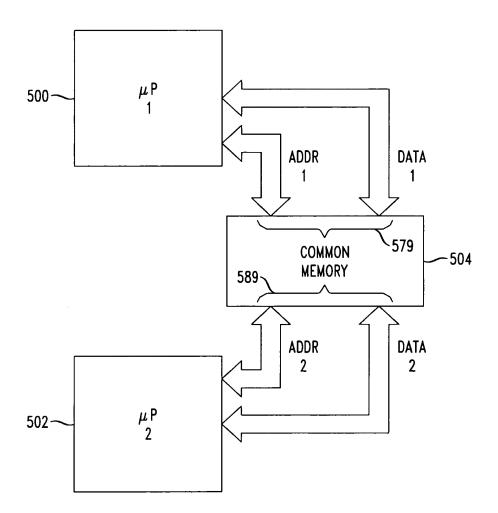
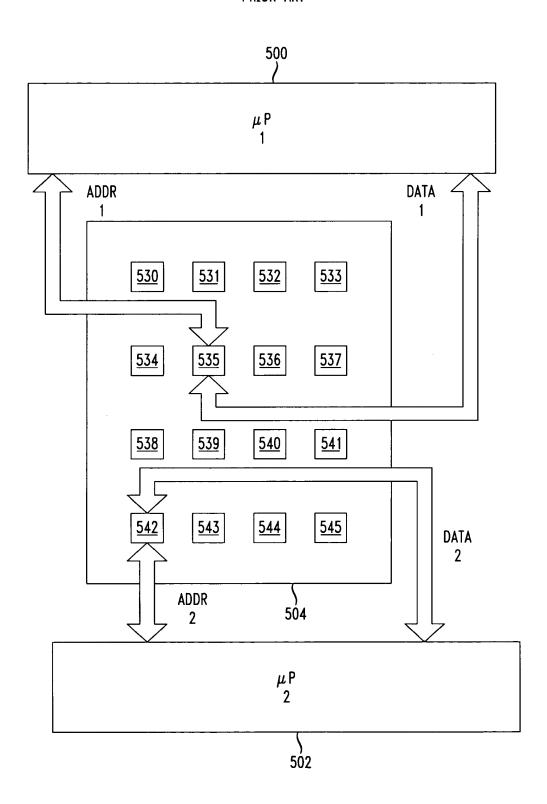




FIG. 5
PRIOR ART



5/5

FIG. 6

